

Specifications

SCT Hardwired Interlock Card

Basic characteristics:

Size: 6U by 220 mm, single width

Power input: 50~70mA at 5V

Signal input: 13 interlock signals (RS422)

Signal output: 13 interlock signals (TTL)

Function description:

Hardwired interlock card provides signal conversion between RS422 inputs and TTL outputs. The failsafe design will be made by using additional pull-down and pull-up resistors on the RS422 inputs.

An additional function is the glitch suppression on the inputs to avoid HW interlock malfunction. The input signals should have some minimal duration (~0.1 ms).

The output signals are low active, i.e. low level = power channel is active.

Two input connectors, one for 12 signals (25-pin D-SUB male) and second one for one VCSEL interlock signal (9-pin D-SUB male), are mounted on the crate rear panel.

One 96-pin 603-2-IEC-C096 connector is used for the powering and the input and output signals from the card to the back plane.

Reference

Version Draft 1.0

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