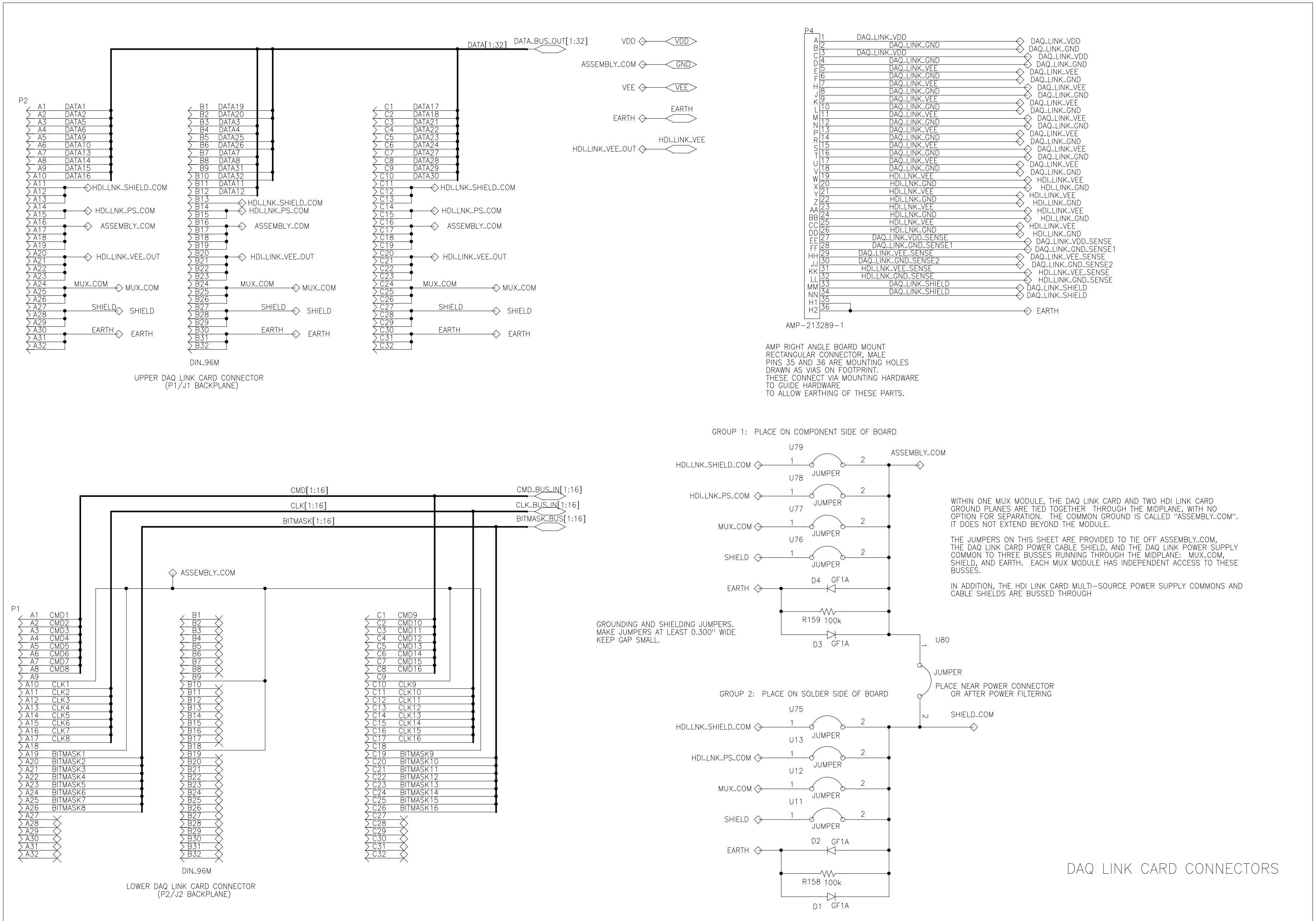
P2
DATA[1:32]
DATA_BUS_OUT[1:32]
VDD
ASSEMBLY_COM
GND
VEE
EARTH
HDI_LINK_VEE_OUT
HDI_LINK_VEE
HDI_LNK_SHIELD_COM
HDI_LNK_PS_COM
HDI_LINK_VEE_OUT
MUX_COM
SHIELD
DIN_96M
P4
DAQ_LINK_VDD
DAQ_LINK_GND
DAQ_LINK_VEE
HDI_LNK_VEE
HDI_LNK_GND
DAQ_LINK_VDD_SENSE
DAQ_LINK_GND_SENSE1
DAQ_LINK_VEE_SENSE
DAQ_LINK_GND_SENSE2
HDI_LNK_VEE_SENSE
HDI_LNK_GND_SENSE
DAQ_LINK_SHIELD
AMP-213289-1
P1
CMD[1:16]
CLK[1:16]
BITMASK[1:16]
CMD_BUS_IN[1:16]
CLK_BUS_IN[1:16]
BITMASK_BUS[1:16]
U79
U78
U77
U76
U75
U13
U12
U11
U80
JUMPER
D1
D2
D3
D4
GF1A
R158 100k
R159 100k
SHIELD_COM

UPPER DAQ LINK CARD CONNECTOR
(P1/J1 BACKPLANE)

AMP RIGHT ANGLE BOARD MOUNT
RECTANGULAR CONNECTOR, MALE
PINS 35 AND 36 ARE MOUNTING HOLES
DRAWN AS VIAS ON FOOTPRINT.
THESE CONNECT VIA MOUNTING HARDWARE
TO GUIDE HARDWARE
TO ALLOW EARTHING OF THESE PARTS.

GROUP 1: PLACE ON COMPONENT SIDE OF BOARD

GROUP 2: PLACE ON SOLDER SIDE OF BOARD

PLACE NEAR POWER CONNECTOR
OR AFTER POWER FILTERING

GROUNDING AND SHIELDING JUMPERS.
MAKE JUMPERS AT LEAST 0.300" WIDE
KEEP GAP SMALL.

WITHIN ONE MUX MODULE, THE DAQ LINK CARD AND TWO HDI LINK CARD GROUND PLANES ARE TIED TOGETHER THROUGH THE MIDPLANE, WITH NO OPTION FOR SEPARATION. THE COMMON GROUND IS CALLED "ASSEMBLY_COM". IT DOES NOT EXTEND BEYOND THE MODULE.

THE JUMPERS ON THIS SHEET ARE PROVIDED TO TIE OFF ASSEMBLY_COM, THE DAQ LINK CARD POWER CABLE SHIELD, AND THE DAQ LINK POWER SUPPLY COMMON TO THREE BUSSES RUNNING THROUGH THE MIDPLANE: MUX_COM, SHIELD, AND EARTH. EACH MUX MODULE HAS INDEPENDENT ACCESS TO THESE BUSSES.

IN ADDITION, THE HDI LINK CARD MULTI-SOURCE POWER SUPPLY COMMONS AND CABLE SHIELDS ARE BUSSED THROUGH

LOWER DAQ LINK CARD CONNECTOR
(P2/J2 BACKPLANE)

NOTE: FERRITE FILTERS ARE OPTIONAL.

DAQ LINK CARD CONNECTORS

DAQ LINK CARD CONNECTORS

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BABAR DAQ LINK CARD
CONNECTORS, GROUNDING AND SHIELDING JUMPERS
CONNECT.SCH
Revision: 4.0
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