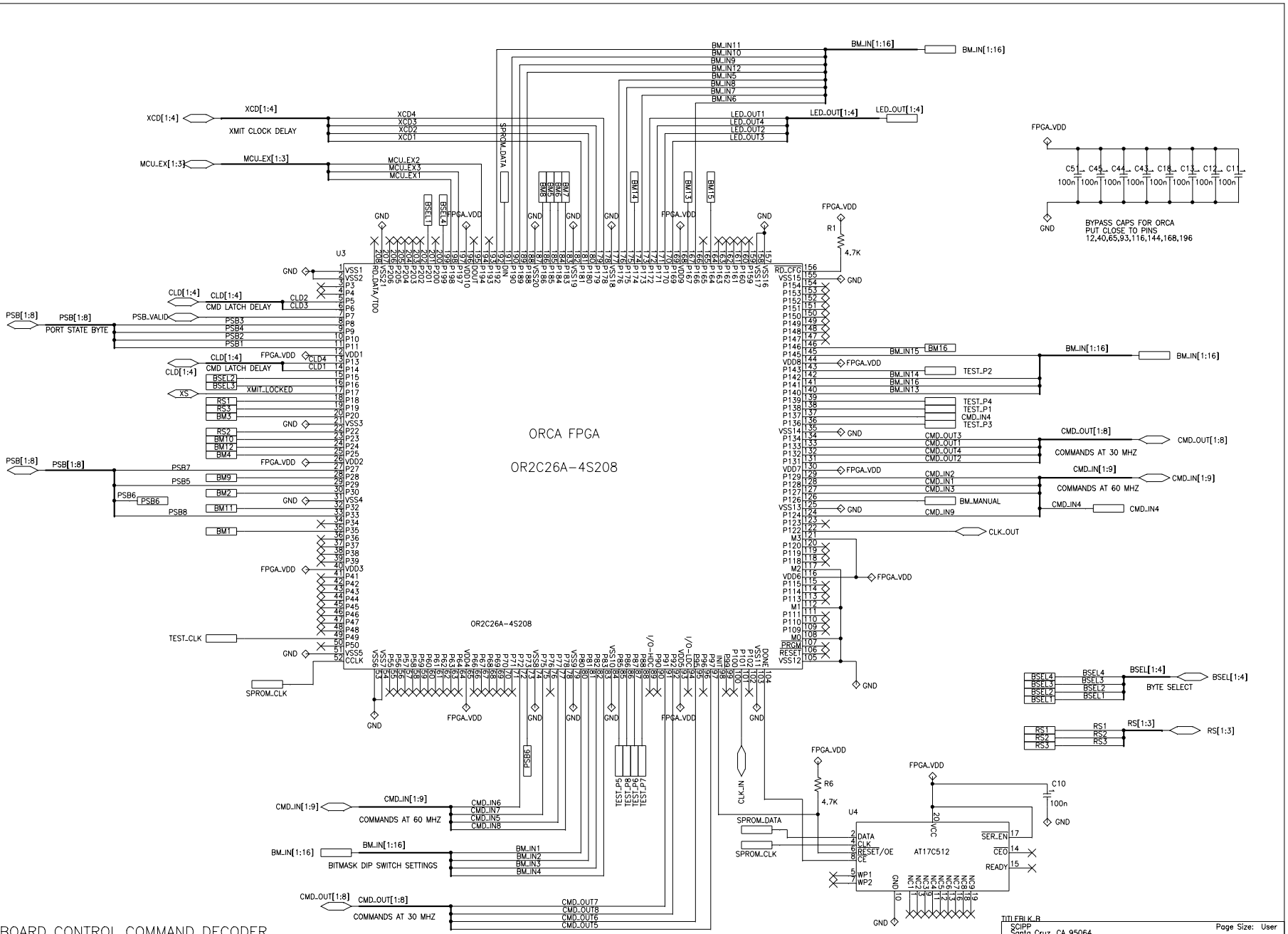
ORCA FPGA
OR2C26A-4S208
BOARD CONTROL COMMAND DECODER
PROVIDES 30 MHZ CLOCK, SYNCHRONIZED
DECODES COMMAND LINES, FIFO FUNCTION
SET BITMASK
MONITOR G-LINK STATUS AND TIME DELAYS.
BYPASS CAPS FOR ORCA
PUT CLOSE TO PINS
12,40,65,93,116,144,168,196
XMIT CLOCK DELAY
CMD LATCH DELAY
PORT STATE BYTE
XMIT_LOCKED
COMMANDS AT 60 MHZ
COMMANDS AT 30 MHZ
BITMASK DIP SWITCH SETTINGS
BYTE SELECT
AT17C512
SCIPP
Santa Cruz, CA 95064
408-459-2694
PC350-700-20-C02
BABAR DAQ LINK CARD
BOARD CONTROL COMMAND DECODER
FPGA01.SCH
Revision: 4.0
4/28/98
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