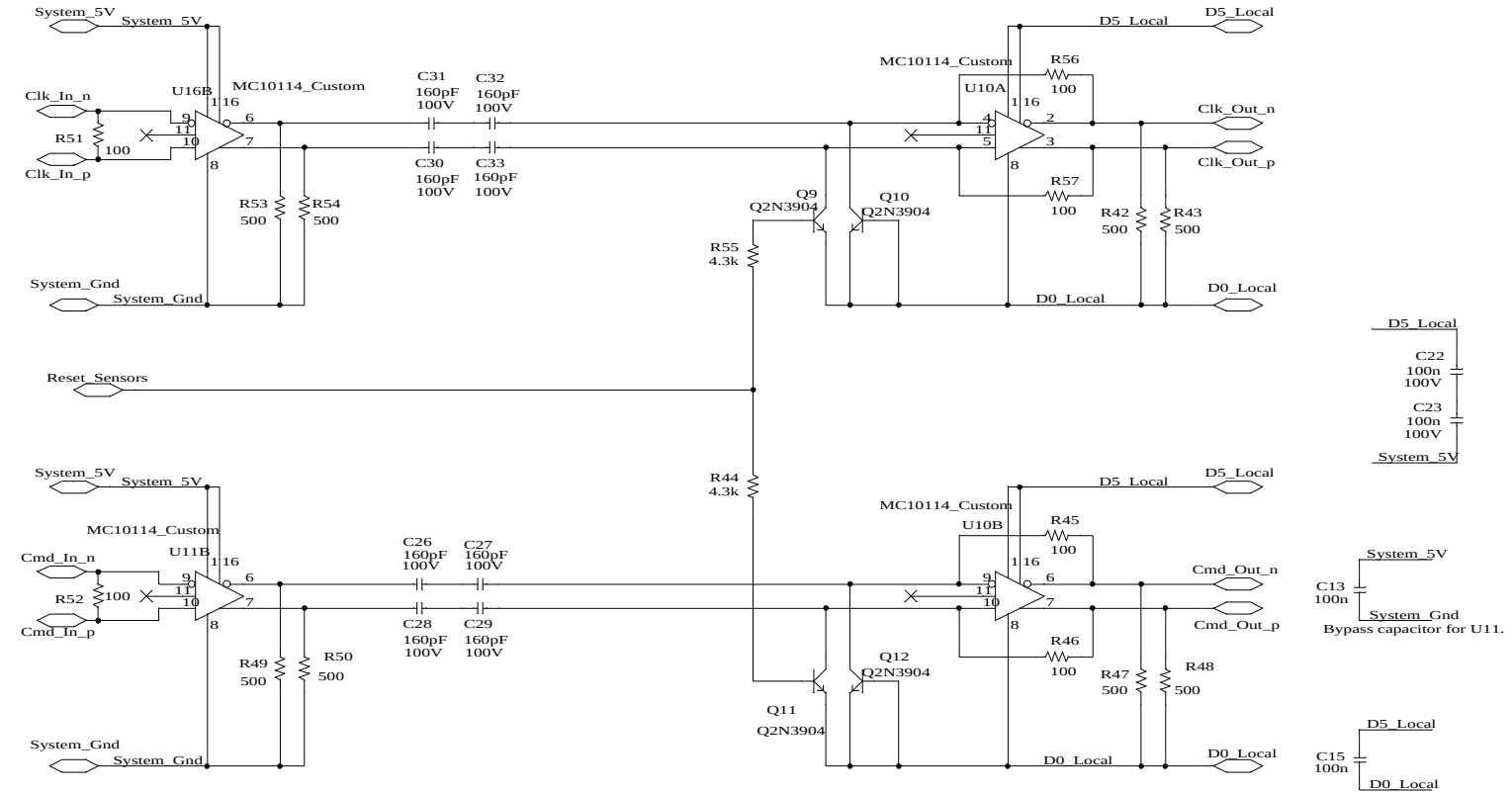

This circuit block buffers and voltage translates clock and command, signals for one side of one HDI module.

Voltage translation takes place in voltage mode. It is essential to AC couple the HDI readout electronics to the system level electronics to eliminate large common-mode signals on the voltage translation circuitry.

TITLEBLK_B

SCIPP
Santa Cruz, CA 95064
408-459-2694

Page Size: B

PRELIMINARY

BABAR HDI Link Card
Clock and Command Voltage Translators
CCXlat1.sch

Revision1.20 2/25/97 Page 1 of 1