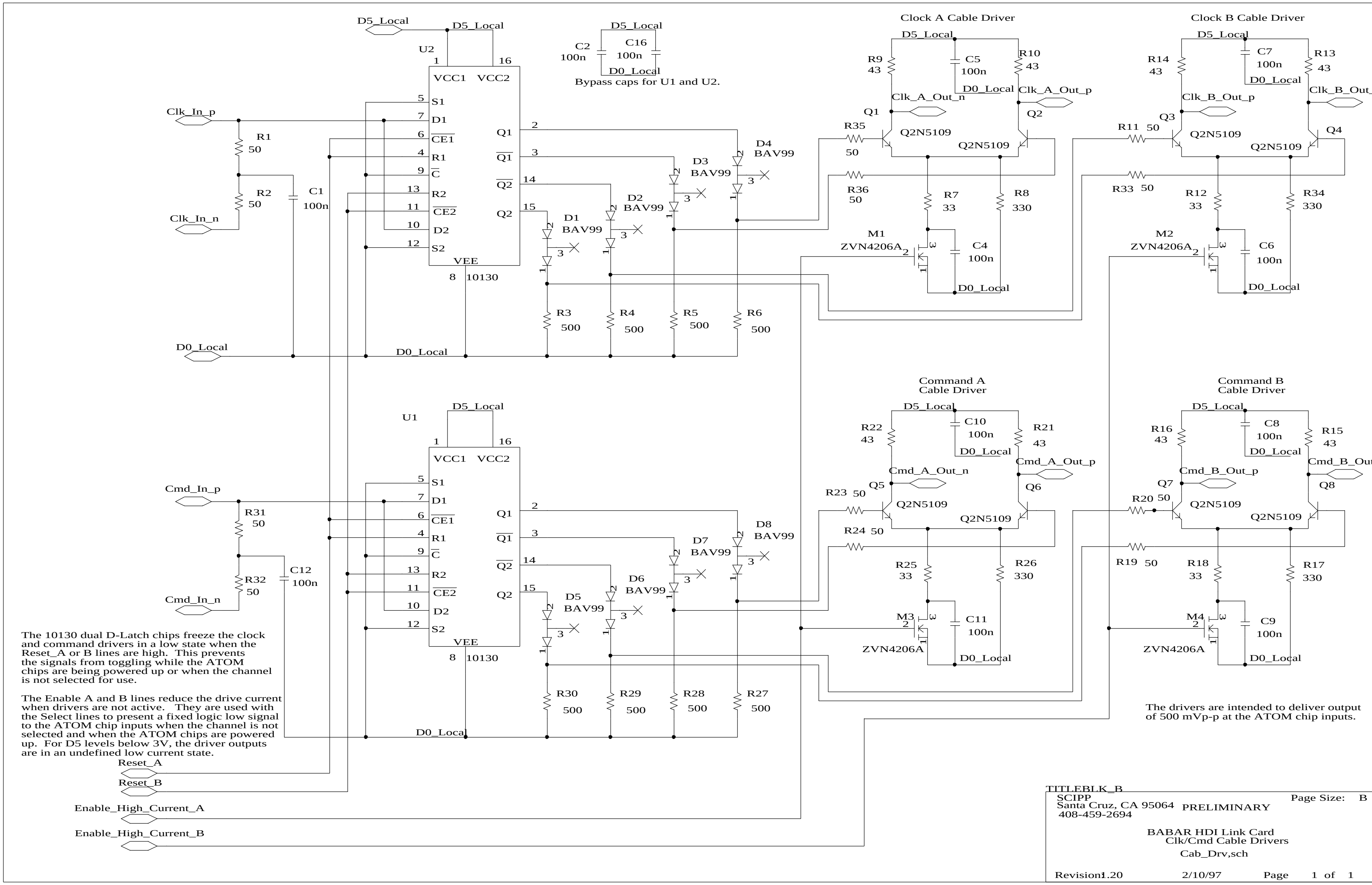

The 10130 dual D-Latch chips freeze the clock and command drivers in a low state when the Reset_A or B lines are high. This prevents the signals from toggling while the ATOM chips are being powered up or when the channel is not selected for use.

The Enable A and B lines reduce the drive current when drivers are not active. They are used with the Select lines to present a fixed logic low signal to the ATOM chip inputs when the channel is not selected and when the ATOM chips are powered up. For D5 levels below 3V, the driver outputs are in an undefined low current state.

The drivers are intended to deliver output of 500 mVp-p at the ATOM chip inputs.

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PRELIMINARY

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BABAR HDI Link Card
Clk/Cmd Cable Drivers

Cab_Drv,sch

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