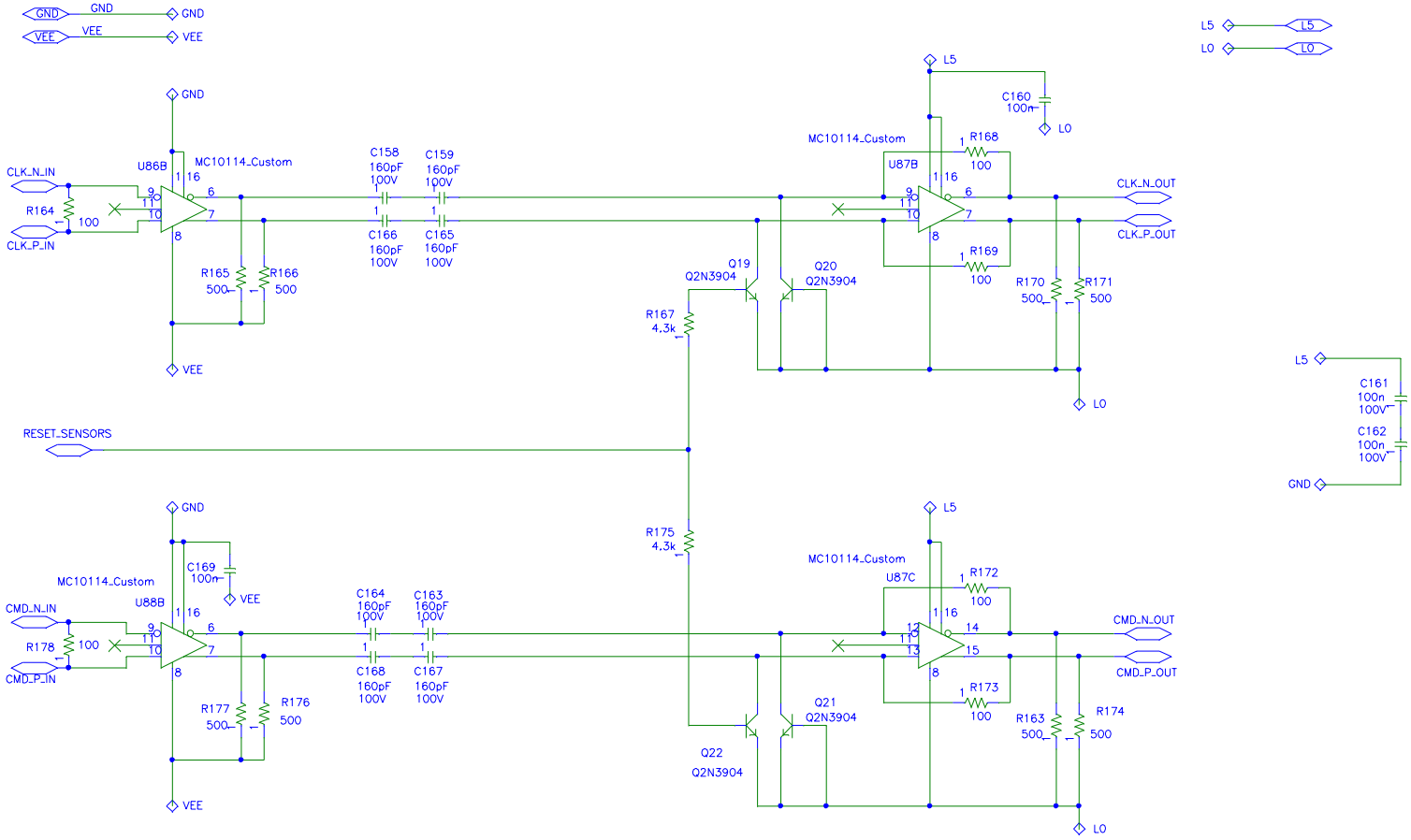

THIS CIRCUIT BLOCK BUFFERS AND VOLTAGE TRANSLATES CLOCK AND COMMAND SIGNALS FOR ONE SIDE OF ONE HDI MODULE.

VOLTAGE TRANSLATION TAKES PLACE IN VOLTAGE MODE.
IT IS ESSENTIAL TO AC COUPLE THE HDI READOUT ELECTRONICS TO THE SYSTEM LEVEL (ASSEMBLY_COM, VEE) ELECTRONICS TO ELIMINATE LARGE COMMON-MODE SIGNALS ON THE VOLTAGE TRANSLATION CIRCUITRY.

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BABAR HDI Link Card
Clock and Command Voltage Translators

SLAC DRAWING NO. SD350-700-15-C03
REVISION: 3 6/10/98

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