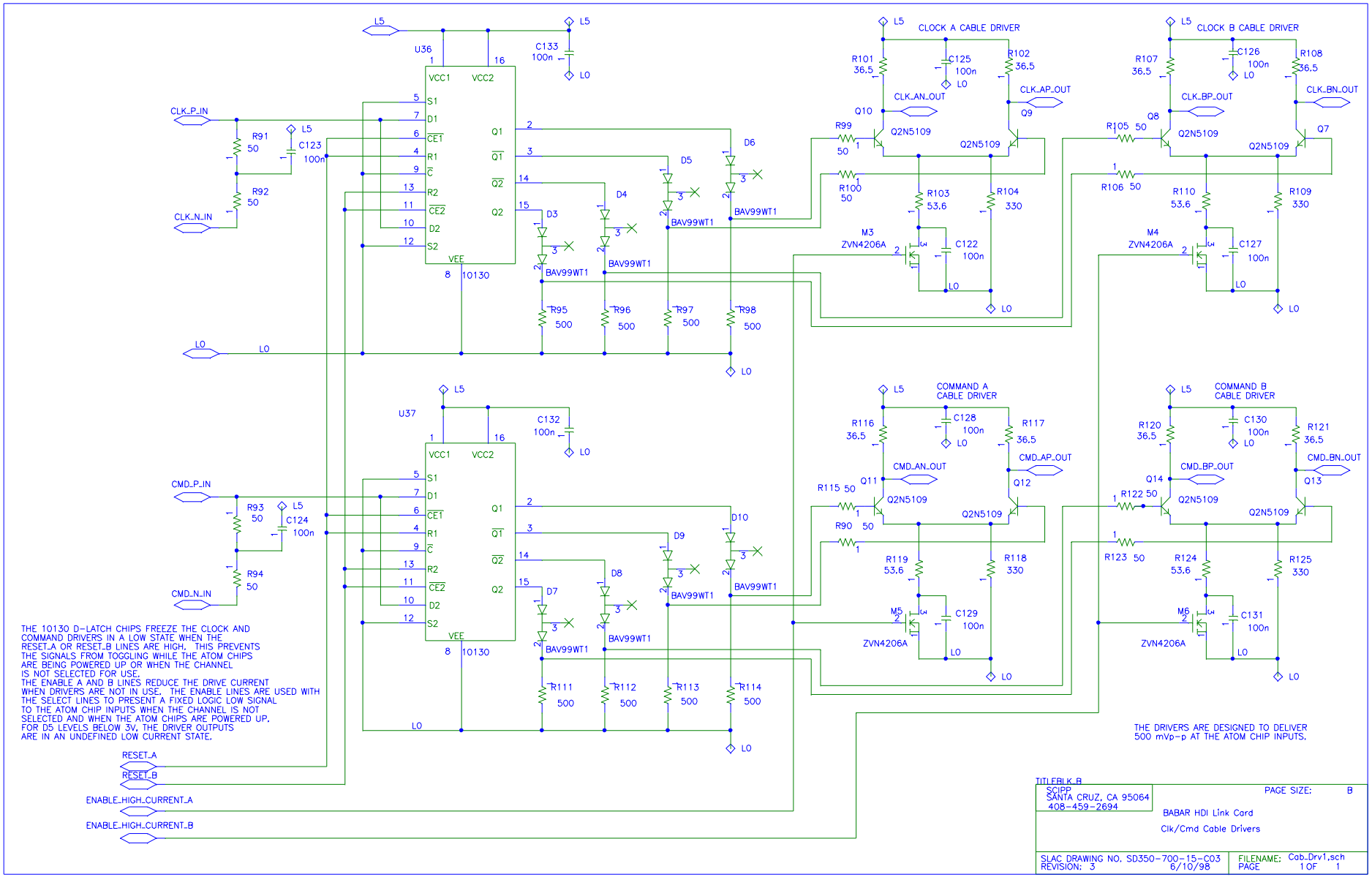
U36
U37
10130
VCC1
VCC2
VEE
L5
L0
C133 100n
C132 100n
CLK_P_IN
CLK_N_IN
CMD_P_IN
CMD_N_IN
R91 50
R92 50
R93 50
R94 50
C123 100n
C124 100n
R95 500
R96 500
R97 500
R98 500
R111 500
R112 500
R113 500
R114 500
D3
D4
D5
D6
D7
D8
D9
D10
BAV99WT1
CLOCK A CABLE DRIVER
CLOCK B CABLE DRIVER
COMMAND A CABLE DRIVER
COMMAND B CABLE DRIVER
R101 36.5
R102 36.5
R107 36.5
R108 36.5
R116 36.5
R117 36.5
R120 36.5
R121 36.5
C125 100n
C126 100n
C128 100n
C130 100n
CLK_AN_OUT
CLK_AP_OUT
CLK_BN_OUT
CMD_AN_OUT
CMD_AP_OUT
CMD_BN_OUT
Q2N5109
ZVN4206A
R99 50
R100 50
R103 53.6
R104 330
R105 50
R106 50
R109 330
R110 53.6
R115 50
R90 50
R118 330
R119 53.6
R122 50
R123 50
R124 53.6
R125 330
C122 100n
C127 100n
C129 100n
C131 100n
RESET_A
RESET_B
ENABLE_HIGH_CURRENT_A
ENABLE_HIGH_CURRENT_B
THE 10130 D-LATCH CHIPS FREEZE THE CLOCK AND COMMAND DRIVERS IN A LOW STATE WHEN THE RESET_A OR RESET_B LINES ARE HIGH. THIS PREVENTS THE SIGNALS FROM TOGGLING WHILE THE ATOM CHIPS ARE BEING POWERED UP OR WHEN THE CHANNEL IS NOT SELECTED FOR USE.
THE ENABLE A AND B LINES REDUCE THE DRIVE CURRENT WHEN DRIVERS ARE NOT IN USE. THE ENABLE LINES ARE USED WITH THE SELECT LINES TO PRESENT A FIXED LOGIC LOW SIGNAL TO THE ATOM CHIP INPUTS WHEN THE CHANNEL IS NOT SELECTED AND WHEN THE ATOM CHIPS ARE POWERED UP. FOR D5 LEVELS BELOW 3V, THE DRIVER OUTPUTS ARE IN AN UNDEFINED LOW CURRENT STATE.
THE DRIVERS ARE DESIGNED TO DELIVER 500 mVp-p AT THE ATOM CHIP INPUTS.
TITLEBLK_B
SCIPP
SANTA CRUZ, CA 95064
408-459-2694
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BABAR HDI Link Card
Clk/Cmd Cable Drivers
SLAC DRAWING NO. SD350-700-15-C03
REVISION: 3
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