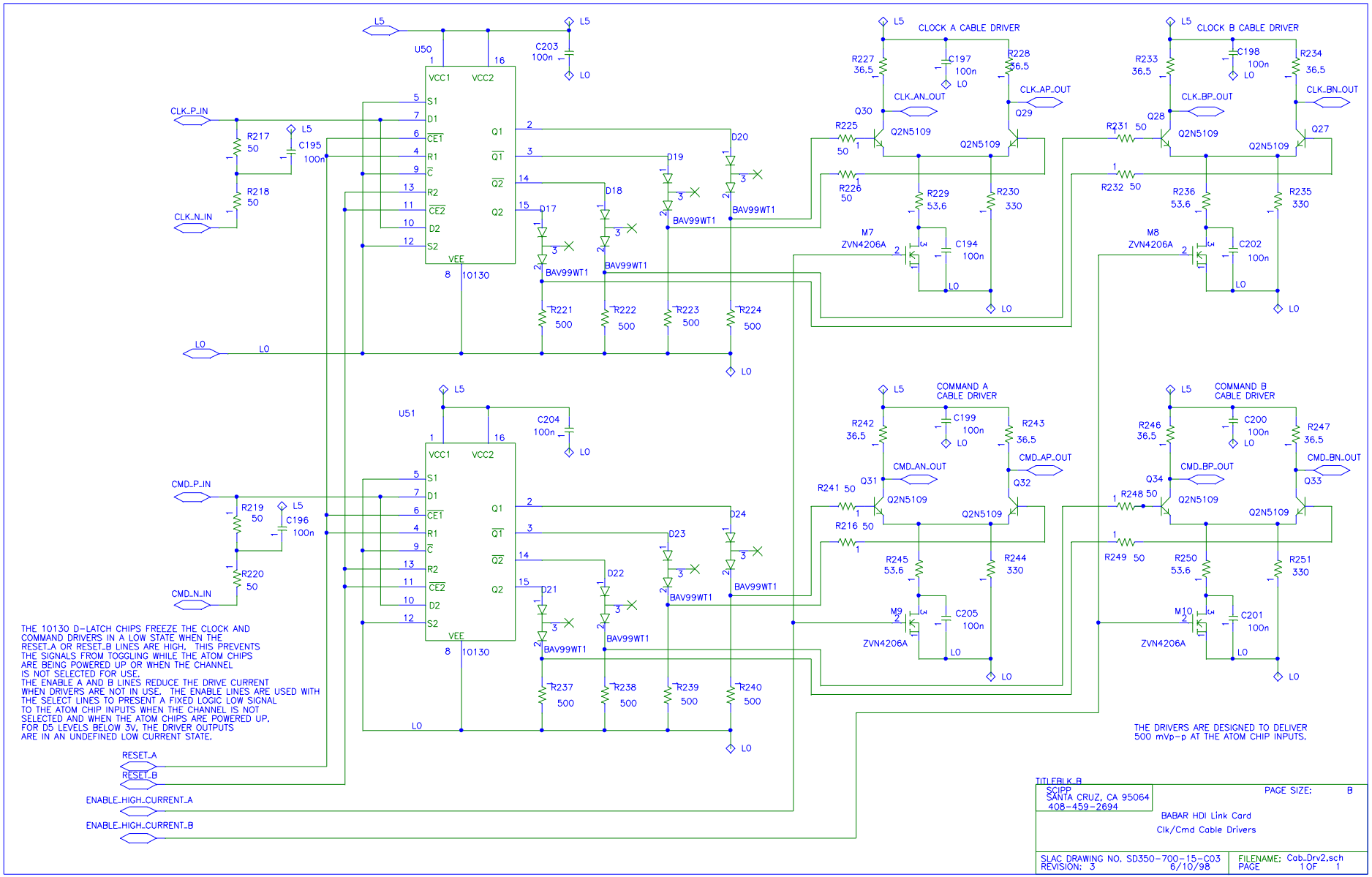
L5
L0
U50
U51
VCC1
VCC2
VEE
10130
C203 100n
C204 100n
CLK_P_IN
CLK_N_IN
CMD_P_IN
CMD_N_IN
R217 50
R218 50
R219 50
R220 50
C195 100n
C196 100n
D17
D18
D19
D20
D21
D22
D23
D24
BAV99WT1
R221 500
R222 500
R223 500
R224 500
R237 500
R238 500
R239 500
R240 500
CLOCK A CABLE DRIVER
CLOCK B CABLE DRIVER
COMMAND A CABLE DRIVER
COMMAND B CABLE DRIVER
C197 100n
C198 100n
C199 100n
C200 100n
R227 36.5
R228 36.5
R233 36.5
R234 36.5
R242 36.5
R243 36.5
R246 36.5
R247 36.5
CLK_AN_OUT
CLK_AP_OUT
CLK_BP_OUT
CLK_BN_OUT
CMD_AN_OUT
CMD_AP_OUT
CMD_BP_OUT
CMD_BN_OUT
Q30
Q29
Q28
Q27
Q31
Q32
Q34
Q33
Q2N5109
R225 50
R226 50
R231 50
R232 50
R241 50
R216 50
R248 50
R249 50
R229 53.6
R230 330
R236 53.6
R235 330
R245 53.6
R244 330
R250 53.6
R251 330
M7
M8
M9
M10
ZVN4206A
C194 100n
C202 100n
C205 100n
C201 100n
THE 10130 D-LATCH CHIPS FREEZE THE CLOCK AND COMMAND DRIVERS IN A LOW STATE WHEN THE RESET_A OR RESET_B LINES ARE HIGH. THIS PREVENTS THE SIGNALS FROM TOGGLING WHILE THE ATOM CHIPS ARE BEING POWERED UP OR WHEN THE CHANNEL IS NOT SELECTED FOR USE.
THE ENABLE_A AND B LINES REDUCE THE DRIVE CURRENT WHEN DRIVERS ARE NOT IN USE. THE ENABLE LINES ARE USED WITH THE SELECT LINES TO PRESENT A FIXED LOGIC LOW SIGNAL TO THE ATOM CHIP INPUTS WHEN THE CHANNEL IS NOT SELECTED AND WHEN THE ATOM CHIPS ARE POWERED UP. FOR D5 LEVELS BELOW 3V, THE DRIVER OUTPUTS ARE IN AN UNDEFINED LOW CURRENT STATE.
THE DRIVERS ARE DESIGNED TO DELIVER 500 mVp-p AT THE ATOM CHIP INPUTS.
RESET_A
RESET_B
ENABLE_HIGH_CURRENT_A
ENABLE_HIGH_CURRENT_B
TITLEBLK_B
SCIPP
SANTA CRUZ, CA 95064
408-459-2694
PAGE SIZE: B
BABAR HDI Link Card
Clk/Cmd Cable Drivers
SLAC DRAWING NO. SD350-700-15-C03
REVISION: 3
6/10/98
FILENAME: Cab_Drv2.sch
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