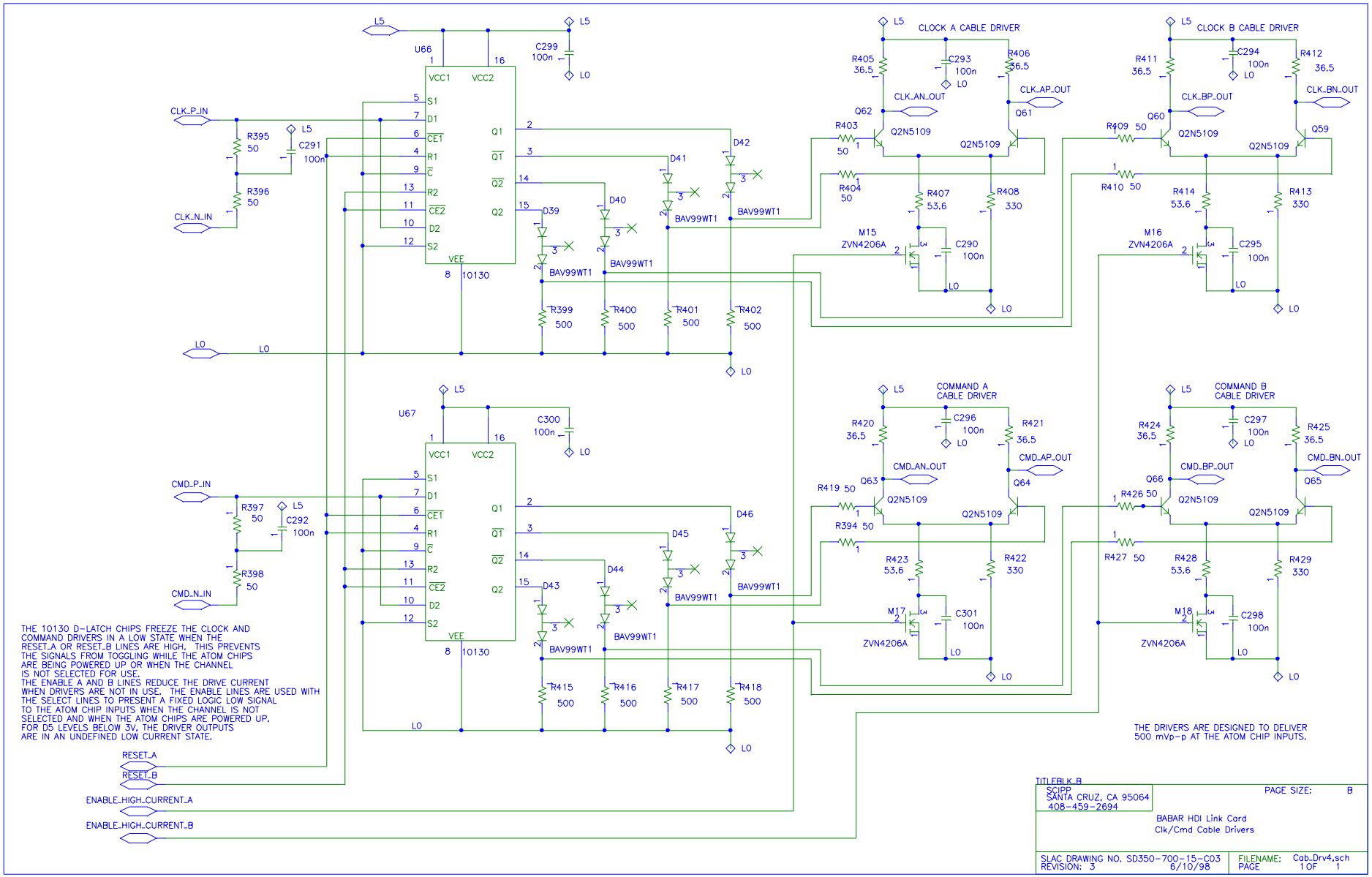
U66
U67
10130
VCC1
VCC2
VEE
S1
D1
CE1
R1
C
R2
CE2
D2
S2
Q1
Q2
L5
L0
C299 100n
C300 100n
CLK_P_IN
CLK_N_IN
CMD_P_IN
CMD_N_IN
R395 50
R396 50
R397 50
R398 50
C291 100n
C292 100n
R399 500
R400 500
R401 500
R402 500
R415 500
R416 500
R417 500
R418 500
D39
D40
D41
D42
D43
D44
D45
D46
BAV99WT1
CLOCK A CABLE DRIVER
CLOCK B CABLE DRIVER
COMMAND A CABLE DRIVER
COMMAND B CABLE DRIVER
R405 36.5
R406 36.5
R411 36.5
R412 36.5
R420 36.5
R421 36.5
R424 36.5
R425 36.5
C293 100n
C294 100n
C296 100n
C297 100n
CLK_AN_OUT
CLK_AP_OUT
CLK_BN_OUT
CMD_AN_OUT
CMD_AP_OUT
CMD_BN_OUT
Q62
Q61
Q60
Q59
Q63
Q64
Q66
Q65
Q2N5109
R403 50
R404 50
R409 50
R410 50
R419 50
R394 50
R426 50
R427 50
R407 53.6
R408 330
R414 53.6
R413 330
R423 53.6
R422 330
R428 53.6
R429 330
M15
M16
M17
M18
ZVN4206A
C290 100n
C295 100n
C301 100n
C298 100n
THE 10130 D-LATCH CHIPS FREEZE THE CLOCK AND COMMAND DRIVERS IN A LOW STATE WHEN THE RESET_A OR RESET_B LINES ARE HIGH. THIS PREVENTS THE SIGNALS FROM TOGGLING WHILE THE ATOM CHIPS ARE BEING POWERED UP OR WHEN THE CHANNEL IS NOT SELECTED FOR USE.
THE ENABLE_A AND B LINES REDUCE THE DRIVE CURRENT WHEN DRIVERS ARE NOT IN USE. THE ENABLE LINES ARE USED WITH THE SELECT LINES TO PRESENT A FIXED LOGIC LOW SIGNAL TO THE ATOM CHIP INPUTS WHEN THE CHANNEL IS NOT SELECTED AND WHEN THE ATOM CHIPS ARE POWERED UP. FOR D5 LEVELS BELOW 3V, THE DRIVER OUTPUTS ARE IN AN UNDEFINED LOW CURRENT STATE.
THE DRIVERS ARE DESIGNED TO DELIVER 500 mVp-p AT THE ATOM CHIP INPUTS.
RESET_A
RESET_B
ENABLE_HIGH_CURRENT_A
ENABLE_HIGH_CURRENT_B
TITLEBLK_B
SCIPP
SANTA CRUZ, CA 95064
408-459-2694
PAGE SIZE: B
BABAR HDI Link Card
Clk/Cmd Cable Drivers
SLAC DRAWING NO. SD350-700-15-C03
REVISION: 3
6/10/98
FILENAME: Cab_Drv4.sch
PAGE 1 OF 1