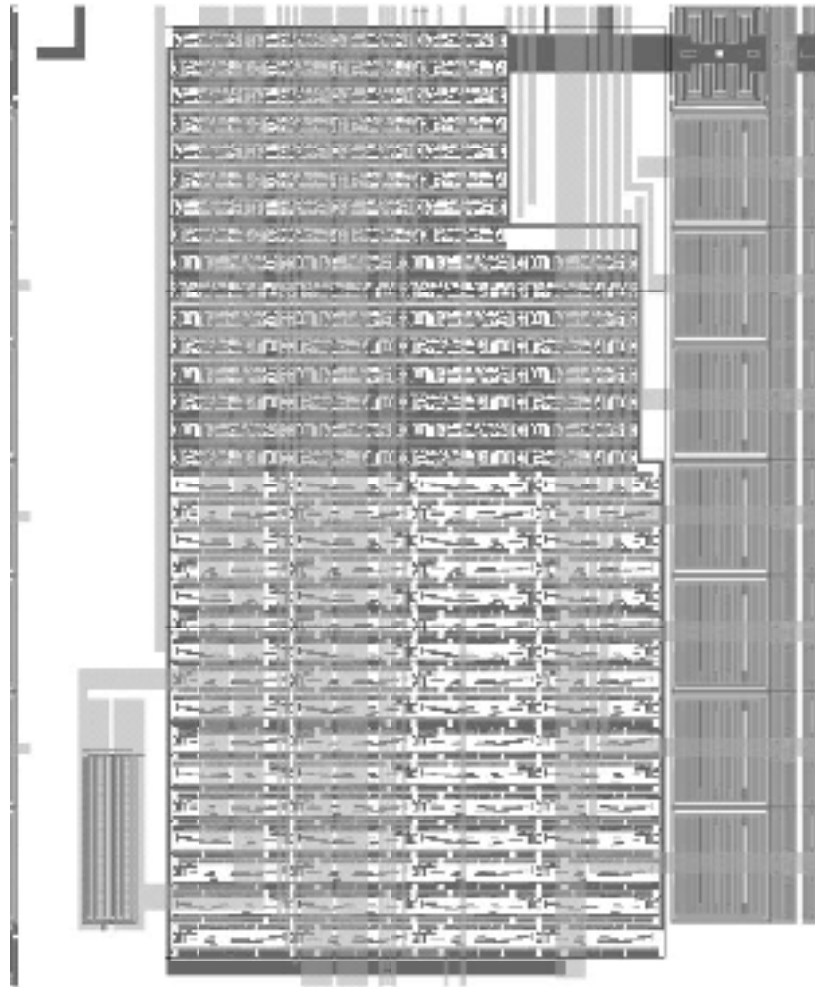
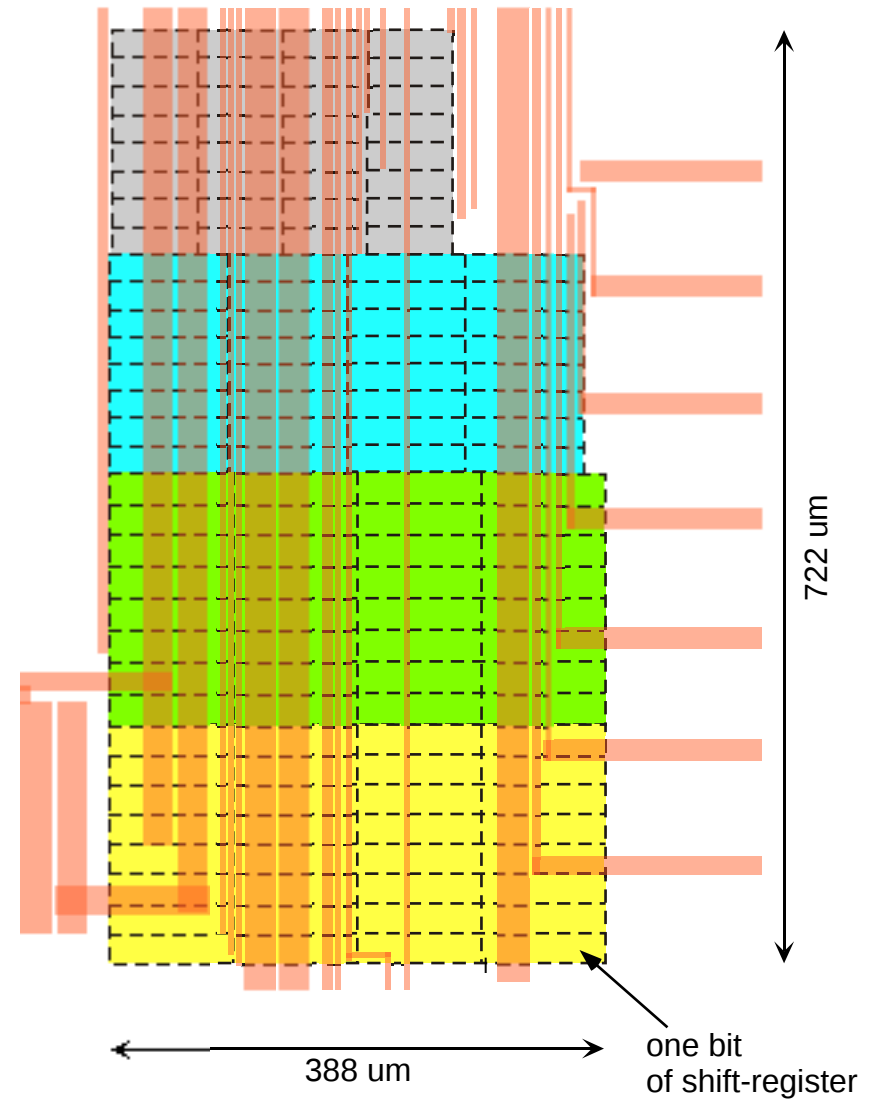


Chip layout of shift-register



Schematic drawing of the left panel



The number of bits in shift register:
 $32 \text{ bits} \times 4 \text{ types} = 128 \text{ bits}$

■ : Metal pattern
■ ■ ■ : Shift-register block of each design