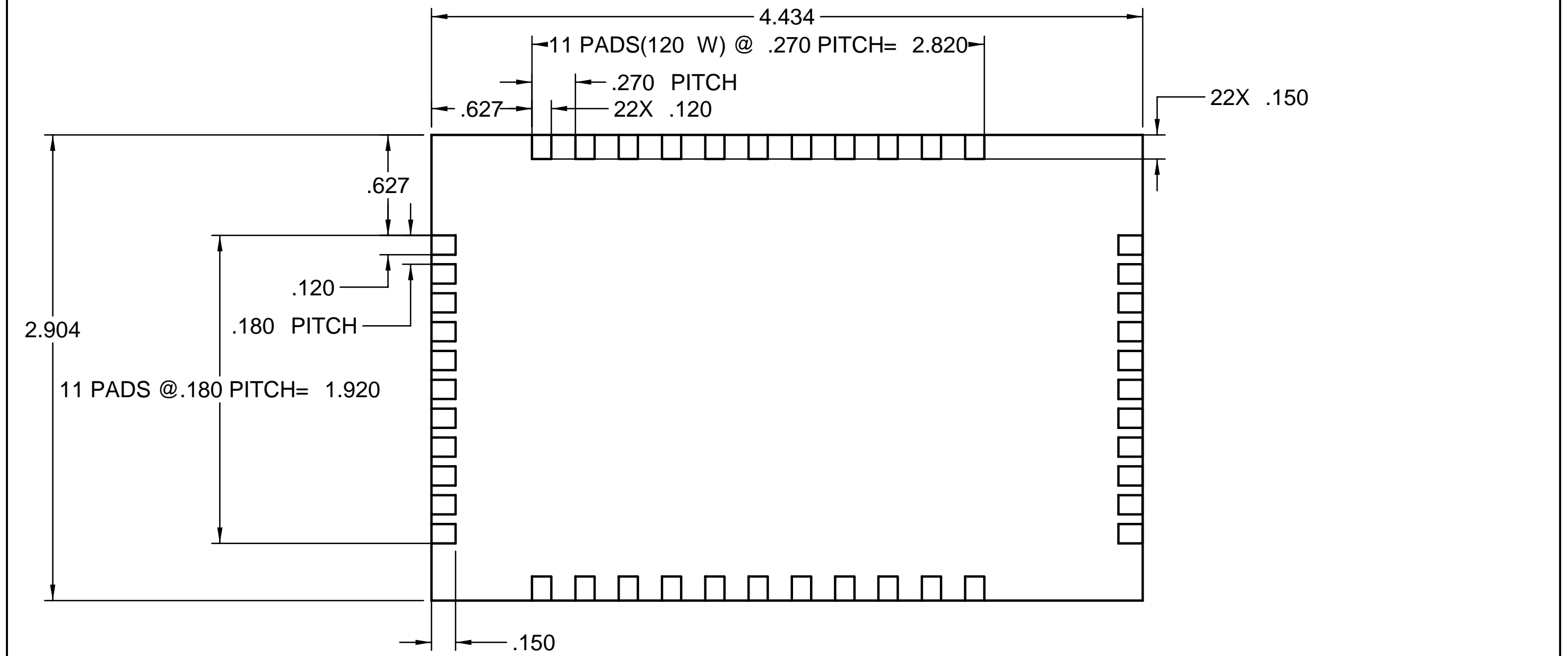


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 PERMISSION OF STANFORD UNIVERSITY

LAT-DS- 1 B

REV	DESCRIPTION	DWN	CHKR	APVD	DATE
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---	SCALE: 40:1	DO NOT SCALE DRAWING	CAD FILE NAME: controller chip 12-20-01.dft		
---	DIMENSIONING AND TOLERANCING IS IN ACCORDANCE WITH ASME Y14.5M-1994.	STANFORD LINEAR ACCELERATOR CENTER U.S. DEPARTMENT OF ENERGY	LAT TRACKER TRAY ASSEMBLY TMCM ASSEMBLY CONTROLLER CHIP		
---	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN MM. TOLERANCES: BREAK EDGES 0.1-0.2 INTERNAL CORNERS R 0.3 MAX	STANFORD UNIVERSITY STANFORD, CALIFORNIA			
---	FRACTIONS ± --- DEC .X ± 0.10 .XX ± .05 .XXX ± --- ANGLE ± ---	ENGR TOM BORDEN DATE DWN BHATNAGAR CHKR D. NELSON	DRAWING NUMBER LAT-DS-		
---	ALL SURF 125/√	APPROVALS TOM BORDEN --- D. NELSON ---	REVISION NUMBER 1	B	

NEXT ASSEMBLIES:

LAT TKR TMCM ASSY CONT CHIP

1 OF 1 SHEET