

Pin assignment: Support board + GTRC probe card

Support board		60-line cable		Signals on GTRC	
Pos	Neg	Pos	Neg	Name	Pad #
P6-13	P6-14	1	2	TACKBP/M	42/43
P6-1	P6-2	3	4	SCMD_OUTP/M	40/41
P6-3	P6-4	5	6	CLKBP/M	38/39
P6-5	P6-6	7	8	NTREQP/M	27/28
P6-7	P6-8	9	10	NSDATAP/M	18/19
P6-9	P6-10	11	12	NTOKEN_OUTP/M	14/15
P7-1	P7-2	13	14	NRESETP/M	29/30
P7-3	P7-4	15	16	NTACKP/M	25/26
P7-5	P7-6	17	18	CLKP/M	22/24
P7-7	P7-8	19	20	NSCMDP/M	20/21
P7-9	P7-10	21	22	NSDATA_INP/M	16/17
P7-11	P7-12	23	24	NTOKENP/M	11/13
P7-13	P7-14	25	26	RD_INP/M	5/6
P7-15	P7-16	27	28	TREQ_INP/M	3/4
P7-21	P7-22	29	30	SE	44
P7-23	P7-24	31	32	A3	35
P7-25	P7-26	33	34	A2	33
P7-27	P7-28	35	36	A1	32
P7-29	P7-30	37	38	A0	31
P7-31	P7-32	39	40	LEFT	9
P7-33	P7-34	41	42	RESETB	8
P7-35	P7-36	43	44	CTRLREG	7
P7-43	P7-44	45	46	SDI	2
P12-13	P12-14	47	—	CLKBP*	38
P12-15	P12-16	48	—	CLKBM*	39
P12-17	P12-18	49	—	NTREQP*	27
P12-19	P12-20	50	—	NTREQM*	28
P12-21	P12-22	51	—	NSDATAP*	18
P12-23	P12-24	52	—	NSDATAM*	19
P12-25	P12-26	53	—	NTOKEN_OUTP*	14
P12-27	P12-28	54	—	NTOKEN_OUTM*	15
P10-3	P10-2	55	56	VDD/GND	12,34/1,23
(P11-3)	(P11-2)	57	58	2.5 V/GND	—
P11-3	P11-2	59	60	3.3 V/GND	—

*) Sense line to measure LVDS voltage swings