

# Senior Thesis Outline

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1. Introduction
  - (a) Background on beam test hardware
  - (b) Background on pCT
  - (c) Experimental issues
2. DAQ System Design
  - (a) Fanout board rework
  - (b) FPGA hardware design
  - (c) CTC integration
  - (d) Software
3. System Verification
  - (a) Pulse injection
  - (b) Noise and source measurement
4. Results
  - (a) Pulse injection
  - (b) Noise and source measurement
  - (c) Beam runs at LLUMC
5. Conclusion
  - (a) Success of experiment
  - (b) Further development of this experiment